

Imperative Synchronous Languages

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Introduction

- in this chapter, we consider the description of synchronous systems by **imperative synchronous languages**
- a synchronous system **works in cycles**, and in each cycle,
 - it reads all inputs $\mathcal{I}$
 - it computes all outputs $\mathcal{O}$ w.r.t. the internal state $\mathcal{S}$
 - it updates the internal state $\mathcal{S}$ for the next cycle

each such computation step is called a **reaction or macro step**

- we must therefore describe the function $\mathcal{S} \times \mathcal{I} \rightarrow \mathcal{S} \times \mathcal{O}$
- it could be done by **synchronous DPNs**, where all nodes read one value from all inputs and produce one value on each output

Imperative Synchronous Languages

- in this chapter, we do not consider synchronous DPNs, and instead, consider **imperative synchronous languages**
 - in general, these languages are obtained from any sequential imperative language by
 - introducing a **new statement pause**
 - and thereby the distinction between micro- and macro steps:
 - all actions between two **pause** statements belong to one reaction, i.e. **one macro step**
 - these actions are called **micro steps**
 - all micro steps refer to the same variable environment
- thus, one often says that **micro steps are executed in zero time**

Understanding Synchronous Languages

Example

```
x = 3;  
w1: pause;  
x = 5;  
y = x;  
w2: pause;
```

- in the first reaction, x is 3
- then, the control stops at w1:**pause**
- in the second reaction, x and y are both 5
- then, the control stops at w2: **pause**

Understanding Synchronous Languages

Example

```
x = 3;  
w1: pause;  
y = x;  
x = 5;  
w2: pause;
```

- this program is equivalent to the previous one, i.e.
 - in the first reaction, x is 3
 - in the second reaction, x and y are both 5
- the ordering of the micro steps in a macro step does not matter
 - macro steps are **sets** of micro steps
 - micro steps are executed by respecting data dependencies
 - i.e., first execute $x = 5$ then $y = x$

synchronous programs dynamically reorder micro steps so that these are executed as if the complete variable environment would already be given at the beginning of the macro step

Dynamic Scheduling of Micro Steps

- the previous example might suggest that we could simply reorder the assignments in programs
- however, this does not work in general
- the execution order of micro steps depends on inputs, thus dynamic scheduling is required:

Example

```
pause;  
y1 = y2 &  →  
    x;  
y2 = y1 &  →  
    !x;  
pause;
```

```
pause;  
if(x) {  
    y2 = y1 & !x; // = false due to →  
                x==true  
    y1 = y2 &  x; // = false due to →  
                y2==false  
} else {  
    y1 = y2 &  x; // = false due to →  
                x==false
```

Dynamic Scheduling of Micro Steps

Example

```
w1: pause;  
if(y1) y1 = true;  
if(y1)  
    if(!y2) y2 =  $\rightarrow$   
        true;  
w2: pause;
```

Example

```
w1: pause;  
y1 = y1;  
y2 = y1 & !y2;  
w2: pause;
```

- a second argument against static ordering of assignments is that not only their right hand sides have to be considered, but also their entire trigger conditions
- e.g., this involves conditions of if-statements

Delayed Assignments

Example

```
x = 3;  
z = y;  
next(y) = x;  
w1: pause;  
x = 5;  
z = y;  
w2: pause;
```

- there are also **delayed assignments**
 - their right hand sides are evaluated in the current macro step
 - but the assignment is done in the next macro step
- in the second macro step,
y and z have therefore the value 3

Write Conflicts

Example

```
x = 3;  
x = 5;  
next (y) = 3;  
w1: pause;  
y = 5;  
w2: pause;
```

- each variable must have a unique value in each macro step
- assigning different values to a variable in one macro step is therefore a write conflict
- this is not allowed
- the causality analysis of the compiler can also take care of this
- also, absence of typical runtime errors can be checked thereby
 - division by zero
 - out-of-bound access to array elements

Event and Memorized Variables

Example

```
x = 3;  
w1: pause;  
y = x;  
w2: pause;
```

- in the first step, x is 3, and we do not know y 's value
- in the second step, y should have the same value as x , but which value is that?
- since there is no action that assigns a value to x , the **reaction to absence** takes place
- it depends on the declaration of x
 - if x is an **event variable**, then it is reset in the second macro step to the default value 0
 - if x is a **memorized variable**, then it stores the value 3 of the previous macro step

Thread Interaction

Example

```
module P(event →  
  a, b, c) {  
  {  
    b = true;  
    p: pause;  
    if(a) b = true;  
    r: pause;  
  }  
  ||  
  {  
    q: pause;  
    if(!b) c = true;  
    a = true;  
    s: pause;  
  }  
}
```

- threads must interact with each other for a correct causal execution
- no problem for the compiler, since threads of the program do not necessarily mean threads in the compiled code
- in the synthesis part, we will explain the compilation of synchronous programs to guarded actions
- threads are no longer visible at that representation

Causality Analysis

Example

```
pause ;  
y1 = y2 ;  
y2 = y1 ;  
pause ;
```

- there are programs where no execution ordering can be found
- compilers have to check at compile-time whether this can happen
- **this phase is called causality analysis**
- it assures that for all reachable states and all possible inputs, the micro steps can be executed in an ordering where all values are known when needed
- programs that are causally correct can, in principle, be rewritten to eliminate all cyclic dependencies
- however, this leads to an unavoidable blow-up
[?, ?, ?, ?, ?]

Imperative Synchronous Languages

- the most popular imperative synchronous language is **Esterel** [?, ?, ?, ?, ?]
- we consider our descendent **Quartz**, which is very similar
- also, some **Statecharts** variants are synchronous
- and of course, all digital hardware circuits, and languages used to describe them (however, neither VHDL nor Verilog are synchronous languages!)
- in the following, we consider the Esterel variant Quartz developed at the TU Kaiserslautern in detail

Variants of Synchronous Languages

- some languages like Statemate Statecharts demand that all assignments are delayed assignments
- thus, there are no causality problems in these languages
- other variants differ in the way the reaction to absence is handled
 - some languages make explicit use of absence values $\square$
 - others, like Quartz define a default value for each type
(but compiler optimizations may decide that values are not required and are therefore replaced by $\square$ [?])
- causality may also depend on particular definitions [?, ?, ?]

Our Programming Language Quartz

in the following, the language Quartz is briefly presented

- we start with the syntax of the language
 - declarations: using flows, data types, and storage classes
 - expressions
 - statements
- and proceed then with the (operational) semantics
 - SOS reaction rules
 - SOS transition rules
 - an interpreter based on SOS rules
- finally, we consider some specific issues like
 - causality problems
 - schizophrenia problems

Modules in Quartz

Example

```
module →  
  NAME(<decls>) {  
    <bodyStatement>  
  }
```

- Quartz programs are organized in modules
- a module can be viewed as a class of an object oriented language
- each module is defined by its name NAME, its interface, and its body statement
- modules can be instantiated several times by
 - inserting expressions for the inputs of the module
 - and inserting writable variables for the outputs of the module
- in Quartz, each module must be stored in a single file whose name is the name of the module (with file suffix .qrz)

Variable Declarations

each variable must be declared with the following information

- **information flow**

- determines whether the program can read or write the variable
- input, output, inout, local and label variables

- **data type**

- determines the possible values a variable may have
- typical types: booleans, nat, int, real, arrays, tuples

- **storage class**

- determines the value if no assignment is executed on a variable
- memorized variables store their previous value
- event variables are reset to a default value

Information Flows in Module's Interface

- **input variables**

- are declared by ? in the interface of a module
- input variables can only be read by the module
- their values are given by the environment (or a simulator)

- **output variables**

- are declared by ! in the interface of a module
- output variables can only be written by the module
- their values are uniquely determined by the module itself

- **inout variables**

- are default declarations (without ? or !) in the interface
- inout variables can only be read and written by the module
- values are determined by this module or other modules
- after linking, inout becomes output

Further Information Flows

in addition to the modules' interfaces, there are further variable declarations

- **local variables**

- are declared by local variable statements
- the module can read and write these variables
- but no other module can access these variables
- local variables have a scope given by the syntax of the local variable declaration

- **labels**

- denote control flow locations like **pause** statements
- these variables are implicitly assigned by the control flow
- no assignments are allowed to these variables
- they can also not be read in the programs

Storage Classes

- **event variables**

- are declared by keyword **event**
- the reaction to absence resets these variables to a default value
- these variables behave like wires in a hardware circuit
- typical hardware-style variables

- **memorized variables**

- are declared by keyword **mem** or nothing, since **mem** is the default
- the reaction to absence stores the previous values in these variables
- these variables behave like registers in a hardware circuit
- typical software-style variables

Finite Scalar Data Types

- **bool**
 - has values **false** and **true**
 - has boolean operations **!**, **&**, **|**, **->**, **<->**
- **nat**{*m*}
 - has values $0, \dots, m - 1$
 - has numeric operations **+**, **-**, *****, **/**, **%**
 - and relations **==**, **!=**, **<**, **<=**, **>**, **>=**
- **int**{*m*}
 - has values $-m, \dots, m - 1$
 - has numeric operations **+**, **-**, *****, **/**, **%**, **abs**
 - and relations **==**, **!=**, **<**, **<=**, **>**, **>=**

Infinite Scalar Data Types

in addition to the scalar finite types, there are also infinite types

- **nat**

- has values $0, 1, 2, \dots$
- has numeric operations $+$, $-$, $*$, $/$, $\%$,
- and relations $==$, $!=$, $<$, $<=$, $>$, $>=$

- **int**

- has values $\dots, -2, -1, 0, 1, 2, \dots$
- has numeric operations $+$, $-$, $*$, $/$, $\%$,
- and relations $==$, $!=$, $<$, $<=$, $>$, $>=$

Further Scalar Data Types: Bitvectors

- **`bv{m}`**
 - has values $0 \dots 0b, \dots, 1 \dots 1b$ (bitvectors of lengths m)
 - has bitwise boolean operations `!`, `&`, `|`, `->`, `<->`
 - bitvector operations for bitvectors y and $x = [x_{k-1}, \dots, x_0]$, we have:
 - `x@y` (concatenation)
 - `b::m` generates bitvector of length m consisting of bits b
 - `x{m}` extracts bit x_m from x
 - `x{m:n}` extracts segment $[x_m, \dots, x_n]$ from x
 - `reverse(x)` is $[x_0, \dots, x_{k-1}]$
 - `fromArray(a)` converts boolean array a to a bitvector
- **`bv`**
 - has values $0b, 1b, 00b, 01b, \dots$ (bitvectors of arbitrary lengths)
 - has same operations as **`bv{m}`**
- **bitvectors are *not* boolean arrays!**: in contrast to arrays, only one write operation is allowed per macro step on a bitvector

Compound Data Types

- arrays

- for any type α , $[m]\alpha$ with some constant m is an array of base type α
- access to array elements is written by square delimiters like $x[i+1]$
- index expressions may be any expressions within the allowed range

- tuples

- given $\alpha_1, \dots, \alpha_n$, the type $\alpha_1 * \dots * \alpha_n$ is a tuple type
- one can access elements of a tuple by writing $x.0, \dots, x.(n-1)$
- index expressions must evaluate to constants at compile time

Example Declarations

- **event bool** ?x,y,!z
- **mem bool** ?x,y,!z
- **event** ?x,y,!z (means **event bool** ?x,y,!z)
- **event int**{5} ?x,y,!z
- **event** [7]**int**{5} ?x, **event int** y, **mem bool** !z
- **event** ([7]**int**{5} * **bool**)?x
- [6]([7] **int**{5} * **bv**{4})y
- **bool** ?x,y,!z (means **mem bool** ?x,y,!z)
- **int**{5} ?x,y,!z
- **mem bv**{5} ?x,y,!z
- **event** [7]**int**{5} ?x, **mem int** y, **bool** !z
- ([7] **int**{5} * **bool**)?x
- **event** [6]([7]**int**{5} * **bv**{4})y

Type System

- we have already mentioned typical operators on the slides of the types
- more operators are available like type converters
 - **arr2bv**(τ)
 - **tup2bv**(τ)
 - **nat2bv**(τ, n)
 - **int2bv**(τ, n)
 - **bv2nat**(τ)
 - **bv2int**(τ)

Type System

- Quartz is strongly typed,
it contains neither polymorphic nor dynamic data types
- operations are bit-precise, e.g.

$$\begin{array}{c}
 \frac{\tau : \mathbf{nat}\{m\} \quad \pi : \mathbf{nat}\{n\}}{\tau + \pi : \mathbf{nat}\{m+n-1\}} \\
 \frac{\tau : \mathbf{nat}\{m\} \quad \pi : \mathbf{nat}\{n\}}{\tau * \pi : \mathbf{nat}\{(m-1)*(n-1)+1\}}
 \end{array}
 \qquad
 \begin{array}{c}
 \frac{\tau : \mathbf{int}\{m\} \quad \pi : \mathbf{int}\{n\}}{\tau + \pi : \mathbf{int}\{m+n\}} \\
 \frac{\tau : \mathbf{int}\{m\} \quad \pi : \mathbf{int}\{n\}}{\tau * \pi : \mathbf{int}\{m*n+1\}}
 \end{array}$$

- for a complete list, see [?]
- this allows a compile-time estimation of bounds of expressions

Subtypes

- the type system respects subset inclusions of types, i.e.
 - **bool** is seen as equivalent to **bv**{1}
 - **nat**{m} is contained in **nat**{n} iff $m \leq n$
 - **nat**{m} is contained in **nat**
 - **nat**{m} is contained in **int**{n} iff $m \leq n$
 - **int**{m} is contained in **int**{n} iff $m \leq n$
 - **int**{m} is contained in **int**
 - **nat** is contained in **int**

Quartz Statements

- we next consider the statements of the Quartz language
- most of these statements have been inherited from Esterel [?, ?, ?, ?, ?]
- we first consider a rather complete list of statements
- their behaviors are first informally explained after these lists
- and then presented formally by SOS rules

All Atomic Quartz Statements

assumptions and assertions	
assume (σ);	assumption
assert (σ);	assertion
actions	
$x = \tau$;	immediate assignment
next (x)= τ ;	delayed assignment
emit (x);	immediate boolean signal emission
emit next (x);	delayed boolean signal emission
wait statements	
nothing ;	empty statement
l : pause ;	new macro step
l : halt ;	infinite loop doing nothing
l : await (σ);	delayed wait on condition
l : immediate await (σ);	immediate wait on condition

Sequential, Parallel, and Branching Control Flow

conditional statements	
if (σ) S_1	conditional statement
if (σ) S_1 else S_2	conditional statement
choose S_1 else S_2	nondeterministic choice
case	case statement
(σ_1) do S_1	
...	
(σ_n) do S_n	
default S	
sequential and parallel control flow	
$S_1; S_2$	sequential execution
$S_1 \parallel S_2$ and $S_1 \&\& S_2$	synchronous parallel execution
$S_1 \parallel\parallel S_2$ and $S_1 \&\&\& S_2$	asynchronous parallel execution
$S_1 S_2$ and $S_1 \& S_2$	interleaved parallel execution

Loop Statements

loops	
do S while (σ);	do-loop
while (σ) S	while-loop
loop S	infinite loop
ℓ : loop S each (σ);	triggered infinite loop
(ℓ_1, ℓ_2): every (σ) S	triggered infinite loop
(ℓ_1, ℓ_2): immediate every (σ) S	triggered infinite loop

Abortion and Suspension Statements

abortion	
weak immediate abort S when(σ);	weak immediate abortion
weak abort S when(σ);	weak delayed abortion
immediate abort S when(σ);	strong immediate abortion
abort S when(σ);	strong delayed abortion
suspension	
ℓ : weak immediate suspend S when(σ);	weak immediate suspension
weak suspend S when(σ);	weak delayed suspension
ℓ : immediate suspend S when(σ);	strong immediate suspension
suspend S when(σ);	strong delayed suspension

Miscellaneous Statements

generic sequential and parallel control flow	
choose ($i=\tau..\pi$) S	generic choice
for ($i=\tau..\pi$) S	generic sequence
for ($i=\tau..\pi$) do η S	generic parallel statements where $\eta \in \{ , , , \&, \&\&, \&\&\& \}$
miscellaneous	
$\{ \alpha \ x_1, \dots, x_n; S \}$	local declaration
let ($x=\tau$) S	let-abbreviation
during S_1 do S_2	during statement
final during S_1 do S_2	final during statement
immediate during S_1 do S_2	immediate during statement
immediate final during S_1 do S_2	immediate final during statement
$C : \text{name}(\tau_1, \dots, \tau_n);$	module instantiation
$\{ S \}$	statement block

General Remarks on Statements

- each statement S is started in some macro step $t \in \mathbb{N}$ and may terminate in a step $t + \delta$ ($0 \leq \delta$)
- if $\delta = 0$ holds, S is called **instantaneous**:
 - its execution does not take time
 - execution of S does only cover micro steps
- if S is not instantaneous, the control flow enters S and will stop somewhere inside S to wait for the next macro step
- due to concurrency,
the control flow may rest at several locations
- it is possible, and often desirable, that statements do not terminate

$\text{assume}(\sigma)$ and $\text{assert}(\sigma)$

- assumptions and assertions are instantaneously executed
- $\text{assume}(\sigma)$
 - $\text{assume}(\sigma)$ tells the compiler that σ holds at this location
 - the compiler will not try to verify this, instead it believes the programmer
- $\text{assert}(\sigma)$
 - $\text{assert}(\sigma)$ specifies that σ should be checked at this location
 - verification tools will check it, and programs will fail if an assertion is violated
- assumptions and assertions are a nice way to specify properties
- however, they do not work in a modular verification
since they typically depend on the context

$x = \tau$ and $\text{next}(x) = \tau$

- both $x = \tau$ and $\text{next}(x) = \tau$ are instantaneously executed
- x must be a writable variable and τ must be readable
- the type of τ must be contained in the type of x
 - otherwise, an assertion is generated to ensure containment
 - in cases that are clearly unsatisfiable, the type-checking fails
- semantics
 - both statements evaluate the right hand side expression τ in the current macro step to a value v
 - $x = \tau$ immediately assigns v to the writable variable x
 - $\text{next}(x) = \tau$ assigns v to the writable variable x in the next macro step
- a typical error are assignments like $x = x + 1$

`emit(x)` and `emit next(x)`

- `emit(x)` is always instantaneous
- `x` must be a writable event variable of boolean type
- `emit(x)` is an abbreviation for `x = true`
- `emit next(x)` is an abbreviation for `next(x) = true`
- emissions are added for historic reasons
they were used as the assignments for 'event variables' in Esterel

Further Atomic Statements

- `nothing` does nothing and needs no time to do nothing
- `pause`
 - when executed, the control flow stops here
(unless there is a surrounding abortion)
 - the current macro step will then end here
 - in the next macro step, the control is resumed from this place
(unless there is a surrounding suspension)
 - `pause` is therefore never instantaneous
- `halt` waits for the rest of time, i.e., $\text{halt} \equiv \text{loop } \text{pause}$
- the programmer can give the control flow locations defined by `pause` and `halt` names in that $\ell : \text{pause}$ and $\ell : \text{halt}$ is written

[immediate] await(σ)

- **await(σ)**
 - when executed, control moves to **await(σ)**, and the macro step ends
 - when the execution resumes in the next macro step, condition σ is checked
 - if σ holds, **await(σ)** instantaneously terminates
 - otherwise, the control remains at **await(σ)**
- the variant **immediate await(σ)** differs in that σ is also checked at starting time, i.e., when started
 - and σ is true, **immediate await(σ)** behaves as **nothing**
 - if σ is false, **immediate await(σ)** behaves as **await(σ)**

Conditionals

- `if( $\sigma$ )  $S_1$  else  $S_2$`
- if started, evaluate expression σ
 - if σ holds, immediately execute S_1 , otherwise execute S_2
- one may also write `if( $\sigma$ )  $S_1$` as abbreviation for `if( $\sigma$ )  $S_1$  else nothing`
- more general form:

$$\left[\begin{array}{l} \text{case} \\ (\sigma_1) \text{ do } S_1 \\ (\sigma_2) \text{ do } S_2 \\ \vdots \\ (\sigma_n) \text{ do } S_n \\ \text{default } S_{n+1} \end{array} \right] \equiv \left[\begin{array}{l} \text{if}(\sigma_1) S_1 \\ \text{else if}(\sigma_2) S_2 \\ \vdots \\ \text{else if}(\sigma_n) S_n \\ \text{else } S_{n+1} \end{array} \right]$$

Nondeterministic Choice

- `choose  $S_1$  else  $S_2$`
 - whenever started, a nondeterministic choice is made to decide whether S_1 or S_2 is executed
 - thus, it behaves like `if(x)  $S_1$  else  $S_2$` with an oracle input x
- the statement is not intended for implementing deterministic controllers
- it is, however, useful for modeling the behavior of environments
- and also for writing test cases for simulation

$S_1 ; S_2$

- sequence $S_1 ; S_2$ is executed as follows
 - when started at time t , start S_1 immediately at time t
 - if S_1 terminates at time $t + \delta_1$, then S_2 is started at time $t + \delta_1$
 - note that $\delta_1 = 0$ may hold, which implies that S_1 and S_2 are then both started at time t
 - $S_1 ; S_2$ terminates if S_2 terminates
 - $S_1 ; S_2$ is instantaneous if both S_1 and S_2 are instantaneous
- moving the control from S_1 to S_2 does not take time

$\rightsquigarrow$ the sequence operation does not take time

$S_1 || S_2$

- synchronous parallel $S_1 || S_2$ is executed as follows:
 - if $S_1 || S_2$ is started at time t , S_1 and S_2 are started at time t
 - if S_1 and S_2 terminate at time $t + \delta_1$ and $t + \delta_2$, respectively, then $S_1 || S_2$ terminates at time $t + \max(\{\delta_1, \delta_2\})$
 - as long as the control is inside S_1 and S_2 , both S_1 and S_2 execute their macro steps synchronously in lockstep
 - S_1 and S_2 may interact during concurrent execution
- curly braces $\{\dots\}$ are used to determine priorities to avoid ambiguities due to the grammar:
 $P_1; P_2 || Q_1; Q_2$ is parsed as $P_1; \{P_2 || Q_1\}; Q_2$

$S_1 \mid S_2$

- interleaved parallel $S_1 \mid S_2$ is executed as follows:
 - if $S_1 \mid S_2$ is started at time t , S_1 and S_2 are started at time t
 - if S_1 and S_2 terminate at time $t + \delta_1$ and $t + \delta_2$, respectively, then $S_1 \mid S_2$ terminates at time $t + \max(\{\delta_1, \delta_2\})$
 - as long as the control is inside S_1 and S_2 , a nondeterministic choice is made on whether the step of S_1 or the step of S_2 is executed
- similar to timesharing of tasks running on a single processor

$S_1 ||| S_2$

- asynchronous parallel $S_1 ||| S_2$ of S_1 and S_2 is executed as follows:
 - if $S_1 ||| S_2$ is started at time t , S_1 and S_2 are started at time t
 - if S_1 and S_2 terminate at time $t + \delta_1$ and $t + \delta_2$, respectively, then $S_1 ||| S_2$ terminates at time $t + \max(\{\delta_1, \delta_2\})$
 - as long as the control is inside S_1 and S_2 , at least one of the steps of S_1 and S_2 is executed
- thus, $S_1 ||| S_2$ is somehow the union of $S_1 | S_2$ and $S_1 || S_2$

$S_1 \mid S_2$, $S_1 \parallel S_2$, and $S_1 \parallel \parallel S_2$

```
module →  
  Test(event →  
    [4]bool a, b) {  
    {emit(a[0]);  
     p1: pause;  
     emit(a[1]);  
     p2: pause;  
     emit(a[2]);  
     p3: pause;  
     emit(a[3]);  
    }  
    ||  
    {emit(b[0]);  
     q1: pause;  
     emit(b[1]);  
     q2: pause;  
     emit(b[2]);  
     q3: pause;  
     emit(b[3]);  
    }  
  }
```

- using $\mid$, the only behavior is to emit $a[i]$ and $b[i]$ in step i
- using $\mid$, first $a[0]$ and $b[0]$, and afterwards, exactly one of the $a[i]$ and $b[j]$ is emitted
- using $\parallel$, first $a[0]$ and $b[0]$, and afterwards, either one or both of $a[i]$ and $b[j]$ is emitted
- in all cases, the emissions on a and those of b appear in the order $a[0], \dots, a[3]$ and $b[0], \dots, b[3]$

$S_1 \& S_2$, $S_1 \&\& S_2$ and $S_1 \&\&\& S_2$

- $S_1 \& S_2$, $S_1 \&\& S_2$ and $S_1 \&\&\& S_2$ are variants of $S_1 | S_2$, $S_1 || S_2$ and $S_1 ||| S_2$
 - at starting time, both S_1 and S_2 are started
 - if S_1 and S_2 terminate at time $t + \delta_1$ and $t + \delta_2$, respectively, then $S_1 \& S_2$, $S_1 \&\& S_2$ and $S_1 \&\&\& S_2$ terminate at time $t + \min(\{\delta_1, \delta_2\})$
 - recall that $S_1 | S_2$, $S_1 || S_2$ and $S_1 ||| S_2$ terminate at time $t + \max(\{\delta_1, \delta_2\})$
- **the difference is only the termination**
- the first statement S_i that terminates aborts the execution of the other one

General Remarks on Loop Statements

- Quartz knows several loop statements
 - `loop S`
 - `do S while( $\sigma$ );`
 - `while( $\sigma$ ) S`
 - `loop S each( $\sigma$ );`
 - `every( $\sigma$ ) S`
- these are described on the following slides
- very important: body statement must not be instantaneous
- every macro step should consist of finitely many micro steps

loop S

- $\text{loop } S$ is executed as follows:
 - first, S is executed
 - if S terminates at time $t + \delta$,
then S is again started at time $t + \delta$
- $\rightsquigarrow$ $\text{loop } S$ is equivalent to $S; \text{loop } S$
- we will see later that abortion statements can abort such loops

do S while(σ)

- do S while(σ) is executed as follows:
 - if started at time t , S is started at time t without checking σ
 - if S terminates at time $t + \delta$, then
 - σ is evaluated
 - if σ is true, then do S while(σ) is executed again
 - if σ is false, then the loop terminates

↪ loop S can be rewritten as do S while(true)

`while( $\sigma$ ) S`

- `while( $\sigma$ ) S` is executed as follows:

- first, σ is evaluated
- if σ is true, then do `S` `while( $\sigma$ )` is executed
- if σ is false, then the loop terminates instantaneously

↪ `while( $\sigma$ ) S` can be rewritten as `if( $\sigma$ ) do S while( $\sigma$ )`

loop S each(σ) and every(σ) S

- loop S each(σ) is executed as follows:
 - when started, S is started while ignoring σ
 - while S is running, condition σ is evaluated in each macro step
 - if σ is false, the execution of S is not disturbed
 - if σ is true, then the current execution of S is aborted, and loop S each(σ) is re-started
 - moreover, if S should terminate before σ became true, then the statement waits for the next macro step where σ holds, and re-starts then loop S each(σ)
- [immediate] every(σ) S can be replaced by

[immediate] await(σ); loop S each(σ)

Abortion Statements

- abort comes in four variants:
 - abort S when(σ)
 - weak abort S when(σ)
 - immediate abort S when(σ)
 - weak immediate abort S when(σ)
- abort S when(σ) is executed as follows
 - when started, S is started and σ is ignored
 - while S is running, σ is evaluated in each macro step
 - if σ is false, the execution of S is not disturbed
 - if σ is true, then the current execution of S is aborted, i.e., the abortion statement instantaneously terminates, none of the micro steps to be executed by S in that step are executed
- strong abortion means: check abortion due to σ **before** executing the micro steps of S
- immediate abort S when(σ) means if($!\sigma$) abort S when(σ)

Weak Abortion Statements

- `weak abort S when( $\sigma$ )` is executed as follows
 - when started, S is started and σ is ignored
 - while S is running, σ is evaluated in each macro step
 - if σ is false, the execution of S is not disturbed
 - if σ is true, then
 - the current execution of S is aborted, i.e.,
 - the abortion statement instantaneously terminates,
 - all of the micro steps to be executed by S in that step are nevertheless executed
- weak abortion means: **check abortion due to σ after executing the micro steps of S**
- `weak immediate abort S when( $\sigma$ )` analogously checks also σ at starting time

Suspension Statements

- suspend statement comes also in four variants:
 - suspend S when(σ)
 - weak suspend S when(σ)
 - immediate suspend S when(σ)
 - weak immediate suspend S when(σ)
- **suspend S when(σ)** is executed as follows
 - when started, S is started and σ is ignored
 - while S is running, σ is evaluated in each macro step
 - if σ is false, the execution of S is not disturbed
 - if σ is true, then the current execution of S is suspended, i.e., the control flow remains at the current locations in S , and none of the micro steps to be executed by S in that step are executed
- strong suspension means: **check suspension due to σ before executing the micro steps of S**

Suspension Statements

- **weak suspend S when(σ)** is executed as follows
 - when started, S is started and σ is ignored
 - while S is running, σ is evaluated in each macro step
 - if σ is false, the execution of S is not disturbed
 - if σ is true, then the current execution of S is weakly suspended, i.e., the control flow remains at the current locations in S , and all of the micro steps to be executed by S in that step are executed
- weak suspension means: **check suspension due to σ after executing the micro steps of S**
- weak suspension can implement loops, e.g.

```
weak suspend  
  pause ;  
  next (x) = x+1;  
when (true) ;
```

Generic Statements

- Quartz has several generic statements:
 - `choose(i= $\tau$ .. $\pi$ ) S`
 - `for(i= $\tau$ .. $\pi$ ) S` (generic sequence)
 - `for(i= $\tau$ .. $\pi$ ) do  $\eta$  S` where $\eta \in \{ |, ||, |||, \&, \&\&, \&\&\& \}$
- their meaning is as follows
 - expressions τ and π are evaluated
 - then, variable i is replaced in S by all numbers $\{\tau, \dots, \pi\}$ to obtain instances S_i
 - these instances are then combined by `;`, `|`, `||`, `|||`, `&`, `&&`, `&&&` or `choose` to obtain non-generic statements
- note that all of these operations are associative!

Local Variable Declarations and Let-Abbreviations

- $\{\alpha \ x_1, \dots, x_n; \ S\}$
 - new variables $x_1, \dots, x_n$ are declared
 - they can be read and written in S
 - they are not known outside S
 - shadowing is currently not allowed (i.e., none of the x_i must already exist)
 - α must specify the storage class and the data type of the x_i
- $\text{let}(x=\tau) \ S$
 - simply abbreviates τ in S by x
 - its implementation does not even require a new variable

`[immediate] [final] during $S_1$ do $S_2$`

- S_2 must always be instantaneous
- `during  $S_1$  do  $S_2$` is executed as follows:
 - when started, start S_1 and ignore S_2
 - while S_1 is running, but not terminating, extend the macro steps of S_1 with those of S_2
- `immediate during  $S_1$  do  $S_2$`
adds S_2 also at starting time of S_1
- `final during  $S_1$  do  $S_2$`
adds S_2 also at termination time of S_1
- `immediate final during  $S_1$  do  $S_2$`
adds S_2 both at starting and at termination time of S_1

Example Programs: Button, ABRO, Speed

- to conclude the informal introduction, let's consider some example programs

A Simple Button

Example

```
module bt(event ?pressed ,  
          !stOff ,!stOn) →  
  {  
    loop {  
      abort  
      loop {  
        emit(stOff);  
        pause;  
      }  
      when(pressed);  
      abort  
      loop {  
        emit(stOn);  
        pause;  
      }  
      when(pressed);  
    }  
  }
```

- when started, emit(stOff); is executed
- this is repeated until pressed occurs
- then, emit(stOn); is executed until pressed occurs again
- and this repeats forever

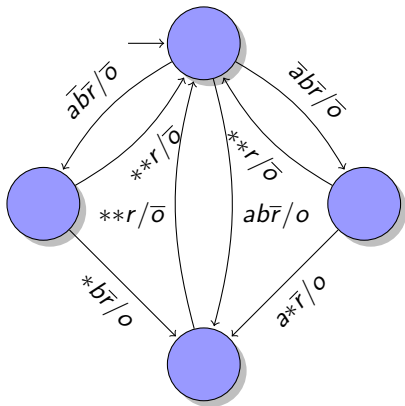
Example: ABRO

ABRO

The system has boolean inputs a , b , r , and an output o . Output o shall be true as soon as both inputs a and b have been true. This behavior should be restarted if r is true.

- problem: what if a , b and r are true at the same time?
- should we make o present?

Mealy Machine for ABRO



- circles are automaton states
 - label $a\bar{b}\bar{r}/o$ means: if $a = \text{true}$ and $b = r = \text{false}$ is read, then output $o = \text{true}$ is generated
 - default behavior: remain in state
 - **finite state machines are perfectly synchronous!**
- ~> use finite state machines to explain the semantics

Quartz Program ABRO

Example (ABRO)

```
module ABRO(event ?a,?b,?r,!o) {  
  loop  
    abort {  
      await(a); || await(b);  
      emit(o);  
      await(r);  
    } when(r);  
}
```

ABRO for More Inputs

Example (ABCRO)

```
module ABCRO(event →  
  ?a,?b,?c,?r,!o) {  
  loop  
    abort {  
      await(a); ||  
      await(b); ||  
      await(c);  
      emit(o);  
      await(r);  
    } when(r);  
}
```

- ABRO can be easily extended to more events
- only add new thread `await(c)`
- for n inputs, program has size $O(n)$
- but the finite state machine has $O(2^n)$ states

Program Speed

Example (Speed)

The system has inputs `cm` and `sec`. If `sec` holds, the number of macro steps where `cm` hold so far (not the current one) should be emitted via signal `speed`. The counter should be reset also at this point of time, and if `cm` should hold together with `sec`, this instance of `cm` should count for the next emission of the speed instead of the current one. This behavior should be repeated forever.

- problem: what if `cm` and `sec` hold at the same time?
- we first exclude this case and consider solutions for that later

Program Speed

Example (Speed (Incorrect))

```
module Speed(event ?cm, ?sec, nat →  
  !speed) {  
  nat dist;  
  loop {  
    dist = 0;  
    abort {  
      every(cm)  
        next(dist) = dist + 1;  
    } when(sec);  
    speed = dist;  
  }  
}
```

- the behavior is quite involved
- we learn later that the system always acts as follows:
 - if sec, assign speed = 0 and dist = 0
 - if !cm&!sec do nothing
 - if cm&!sec, assign **next**(dist) = dist+1
- this yields a write conflict if at some time t , we have cm&!sec and at $t + 1$, we have sec since then dist will be possibly assigned different values
- moreover, if cm&sec holds, cm is not counted!

Weak Abortion in Program Speed

Example (Speed (Incorrect))

```
module Speed(event ?cm, ?sec, nat →  
  !speed) {  
  nat dist;  
  loop {  
    dist = 0;  
    weak abort {  
      every(cm)  
        next(dist) = dist + 1;  
    } when(sec);  
    speed = dist;  
  }  
}
```

- we may use a weak abortion?
- after initialization, we have again two states s0 and s1 with the same dataflow
 - if cm assign **next**(dist) = dist+1
 - if sec assign dist = 0
 - if sec assign speed = dist

⇒ all cm are counted, but write conflict on dist remains

Immediate Abortion in Program Speed

Example (Speed (Incorrect))

```
module Speed(event ?cm,?sec ,nat →
!speed) {
  nat dist;
  loop {
    dist = 0;
    immediate abort {
      every(cm)
        next(dist) = dist + 1;
    } when(sec);
    speed = dist;
  }
}
```

- we may use an immediate abortion?
- after initialization, we have again two states s_0 and s_1 with the same dataflow
 - if $cm \& !sec$ assign **next**(dist) = dist+1
 - if sec assign dist = 0
 - if sec assign speed = dist

⇒ now the write conflict on dist is resolved, by in case of $cm \& !sec$, the cm signal is not counted!

⇒ moreover, the loop body is now instantaneous

Fixing the Problem by Reincarnation of Local Variables

Example (Speed (Correct))

```
module Speed(event ?cm, ?sec, nat →  
  !speed) {  
  loop {  
    weak abort {  
      nat dist;  
      dist = 0;  
      if(cm) next(dist) = dist+1;  
      do {  
        s0:pause;  
        if(cm) next(dist) = →  
          dist+1;  
      } while(!sec);  
      speed = dist;  
    } when(sec);  
  }  
}
```

- the program has then the following actions
 - if $cm \& !sec$, assign **next**(dist) = dist+1
 - if $cm \& sec$, assign **next**(dist) = dist@1+1
 - if sec, assign dist@1 = 0
 - if sec, assign speed = dist

⇒ every cm is counted

⇒ write conflict on dist is resolved by reincarnation

Yet another corrected version

Example (Speed (Correct))

```
module Speed(event ?cm,?sec ,nat →
  !speed) {
  nat dist;
  loop {
    if(sec) speed = dist;
    case
      (!cm&!sec) do next(dist) = dist;
      (!cm& sec) do next(dist) = 0;
      ( cm&!sec) do next(dist) = →
        dist+1;
      ( cm& sec) do next(dist) = 1;
    default nothing;
    s0:pause;
  }
}
```

- the program has then the following actions
 - if !cm&!sec, assign **next**(dist) = dist
 - if !cm& sec, assign **next**(dist) = 0
 - if cm&!sec, assign **next**(dist) = dist+1
 - if cm& sec, assign **next**(dist) = 1
 - if sec, assign speed = dist

⇒ every cm is counted; reset and increment is resolved by assigning 1

⇒ no reincarnation and no additional variable is used

Core Statements

- the informal description of the behavior of statements is way too imprecise
- there is a tight interaction of concurrent control flows
- we have to formally specify the precise behavior of the statements
- in the following, we define an operational semantics
- to this end, we first reduce the set of statements to a smaller set of core statements

Core Statements

<code>nothing</code>	(empty statement)
<code>ℓ : pause</code>	(new macro step)
<code>$x = \tau, \text{next}(x) = \tau$</code>	(assignments)
<code>if(σ) S_1 else S_2</code>	(conditional)
<code>S_1; S_2</code>	(sequence)
<code>$S_1 \parallel S_2$</code>	(synchronous concurrency)
<code>do S while(σ)</code>	(loop)
<code>[weak] [immediate] abort S when(σ)</code>	(abortion statements)
<code>[weak] [immediate] suspend S when(σ)</code>	(suspension statements)
<code>{α x; S}</code>	(local variable declaration)

Simple Macro Definitions

- $\text{emit}(x); \equiv x = \text{true};$
- $\text{emit next}(x); \equiv \text{next}(x) = \text{true};$
- $\ell:\text{halt} \equiv \text{do } \ell:\text{pause}; \text{ while}(\text{true});$
- $\ell:\text{await}(\sigma) \equiv \text{do } \ell:\text{pause}; \text{ while}(!\sigma);$
- $\ell:\text{immediate await}(\sigma) \equiv \text{while}(\sigma) \ell:\text{pause};$
- $\text{while}(\sigma) S \equiv \text{if}(\sigma) \text{ do } S \text{ while}(\sigma);$
- $\text{loop } S \equiv \text{do } S \text{ while}(\text{true});$
- $\text{loop } S \text{ each}(\sigma) \equiv \text{loop abort } \{S; \text{halt};\} \text{ when}(\sigma);$
- $\text{every}(\sigma) S \equiv \text{await}(\sigma); \text{loop } S \text{ each}(\sigma);$
- $\text{immediate every}(\sigma) S \equiv \text{immediate await}(\sigma); \text{loop } S \text{ each}(\sigma);$

Replacing Nondeterminism by Oracles

- the following statements are nondeterministic:
 - `choose  $S_1$  else  $S_2$`
 - $S_1 \mid S_2$ and $S_1 \parallel S_2$
 - $S_1 \& S_2$ and $S_1 \&\&\& S_2$
- we can replace them by asking new boolean input variables for the nondeterministic choices
- in case of `choose  $S_1$  else  $S_2$` , this is trivial
- for the other statements, we wrap each S_i into a suspend statement and ask oracles which of the possible executions should be performed

Eliminating $P \&\& Q$

Example (Eliminating $P \&\& Q$)

```
event tP, tQ;  
  {weak abort  
    P;  
    emit(tP);  
  when(tQ)  
  }  
||  
  {weak abort  
    Q;  
    emit(tQ);  
  when(tP)  
  }
```

Eliminating during P do Q

Example (Eliminating during P do Q)

```
{event t;  
  P;  
  emit(t);  
}  
||  
immediate abort  
  loop {  
    pause;  
    Q;  
  }  
when(t)
```

Eliminating immediate during P do Q

Example (Eliminating immediate during P do Q)

```
{event t;  
  P;  
  emit(t);  
}  
||  
{Q;  
  immediate abort  
  loop {  
    pause;  
    Q;  
  }  
when(t)}
```


Eliminating final during P do Q

Example (Eliminating final during P do Q)

```
{event t;  
  P;  
  emit(t);  
}  
||  
immediate weak abort  
  loop {  
    pause;  
    Q;  
  }  
when(t)
```

Eliminating immediate final during P do Q

Example (Eliminating immediate final during P do Q)

```
{event t;  
  P;  
  emit(t);  
}  
||  
{Q;  
  immediate weak abort  
    loop {  
      pause;  
      Q;  
    }  
  when(t)}
```

Uniqueness of Core Language?

- are there alternatives for defining a core language?
- of course, there are many alternatives, for example:
- `pause;  $\equiv$  await(true);`
- `pause;  $\equiv$  abort halt; when(true)`

Redundancy of Core Language

- some variants of abort and suspend could be eliminated as well
- immediate abort S when(σ) is equivalent to
if(σ) nothing else abort S when(σ)
- however, this is not so simple with the weak version
- even pause can be eliminated

$$\text{pause} \equiv \left[\begin{array}{l} \text{abort} \\ \text{immediate suspend} \\ \text{nothing} \\ \text{when(true)} \\ \text{when(true)} \end{array} \right]$$

- nevertheless, the chosen subset is reasonable

Operational Semantics

- we now formally define the semantics
- it is an operational semantics, thus an interpreter can be implemented this way
- two steps are formalized
 - transition of the control flow for a full variable environment by SOS transition rules
 - computation of the reaction, i.e. the full variable environment of a macro step by SOS reaction rules

SOS Transition Rules

- SOS (structural operational semantics) is a way to describe semantics which goes back to Plotkin [?]
 - SOS transition rules of Quartz describe the movement of the control flow
 - inputs are
 - statement S
 - environment $\mathcal{E}$ (knows the value $\mathcal{E}(x)$ of **each** variable x)
 - outputs are
 - statement S' , which has to be executed next
 - actions $\mathcal{D}$ (assignments) performed by S in this macro step
 - termination flag $b \in \{\text{true}, \text{false}\}$
- ⇒ problem: to apply the rules, one must know the values of all variables, in particular the values of the output variables

SOS Transition Rules

Transition Rule

$$\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, t \rangle$$

- $\mathcal{E}$: environment of the current macro step
- S : statement to be executed
- S' : residual statement for the next micro or macro step (depending on t)
- $\mathcal{D}$: actions that are executed in the current step
- t : instantaneous flag; micro step if true, macro step otherwise

SOS Transition Rules with Assumptions

Transition Rule

$$\frac{\varphi_1 \dots \varphi_n}{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, t \rangle}$$

- some transition rules have assumptions
- if conditions $\varphi_1, \dots, \varphi_n$ are true, then we can conclude that also $\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, t \rangle$ holds

Invariant for Instantaneous Executions

Transition Rule

$$\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, true \rangle$$

- the SOS transition rules maintain the following invariant:
if the instantaneous execution flag is true, we know that S' is equivalent to `nothing`;
- S' could be something like `nothing; | nothing`;

Environments and Evaluation of Expressions

- the environment $\mathcal{E}$ is a function that maps variables to values
- this models the memory of the program
- we write $\llbracket \tau \rrbracket_{\mathcal{E}}$ to evaluate an expression τ in environment $\mathcal{E}$
- for example, $\mathcal{E}(x) = 3$ and $\mathcal{E}(y) = 5$ implies $\llbracket x + y \rrbracket_{\mathcal{E}} = 8$
- due to synchrony, environments are constant within a macro step

Atomic Statements

$$\langle \mathcal{E}, \text{nothing} \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\}, \text{true} \rangle$$

$$\langle \mathcal{E}, \ell : \text{pause} \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\}, \text{false} \rangle$$

$$\langle \mathcal{E}, x = \tau \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{x = \tau\}, \text{true} \rangle$$

$$\langle \mathcal{E}, \text{next}(x) = \tau \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\text{next}(x) = \tau\}, \text{true} \rangle$$

Conditional

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \text{ and } \langle \mathcal{E}, S_1 \rangle \rightarrow_{\mathbb{Q}} \langle S'_1, \mathcal{D}_1, t_1 \rangle}{\langle \mathcal{E}, \text{if}(\sigma) \ S_1 \ \text{else} \ S_2 \rangle \rightarrow_{\mathbb{Q}} \langle S'_1, \mathcal{D}_1, t_1 \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S_2 \rangle \rightarrow_{\mathbb{Q}} \langle S'_2, \mathcal{D}_2, t_2 \rangle}{\langle \mathcal{E}, \text{if}(\sigma) \ S_1 \ \text{else} \ S_2 \rangle \rightarrow_{\mathbb{Q}} \langle S'_2, \mathcal{D}_2, t_2 \rangle}$$

Sequence

$$\frac{\langle \mathcal{E}, S_1 \rangle \rightarrow_{\mathbb{Q}} \langle S'_1, \mathcal{D}_1, \text{false} \rangle}{\langle \mathcal{E}, \{S_1; S_2\} \rangle \rightarrow_{\mathbb{Q}} \langle \{S'_1; S_2\}, \mathcal{D}_1, \text{false} \rangle}$$

$$\frac{\langle \mathcal{E}, S_1 \rangle \rightarrow_{\mathbb{Q}} \langle S'_1, \mathcal{D}_1, \text{true} \rangle \quad \langle \mathcal{E}, S_2 \rangle \rightarrow_{\mathbb{Q}} \langle S'_2, \mathcal{D}_2, t_2 \rangle}{\langle \mathcal{E}, \{S_1; S_2\} \rangle \rightarrow_{\mathbb{Q}} \langle S'_2, \mathcal{D}_1 \cup \mathcal{D}_2, t_2 \rangle}$$

Parallel Statement

$$\frac{\langle \mathcal{E}, S_1 \rangle \twoheadrightarrow_{\mathbb{Q}} \langle S'_1, \mathcal{D}_1, t_1 \rangle \quad \langle \mathcal{E}, S_2 \rangle \twoheadrightarrow_{\mathbb{Q}} \langle S'_2, \mathcal{D}_2, t_2 \rangle}{\langle \mathcal{E}, \{S_1 \parallel S_2\} \rangle \twoheadrightarrow_{\mathbb{Q}} \langle \{S'_1 \parallel S'_2\}, \mathcal{D}_1 \cup \mathcal{D}_2, t_1 \wedge t_2 \rangle}$$

do S while(σ) and while(σ) S

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \text{do } S \text{ while}(\sigma) \rangle \rightarrow_{\mathbb{Q}} \langle \{S'; \text{ while}(\sigma) S\}, \mathcal{D}, \text{false} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false}}{\langle \mathcal{E}, \text{while}(\sigma) S \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \text{while}(\sigma) S \rangle \rightarrow_{\mathbb{Q}} \langle \{S'; \text{ while}(\sigma) S\}, \mathcal{D}, \text{false} \rangle}$$

Strong Delayed Abort

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \text{abort } S \text{ when}(\sigma) \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \text{abort } S \text{ when}(\sigma) \rangle \rightarrow_{\mathbb{Q}} \left\langle \left[\begin{array}{l} \text{immediate abort } S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \right\rangle}$$

Strong Immediate Abort

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true}}{\langle \mathcal{E}, \left[\begin{array}{l} \text{immediate abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{immediate abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{immediate abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \left[\begin{array}{l} \text{immediate abort } S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \rangle}$$

Weak Delayed Abort

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \left[\begin{array}{l} \text{weak immediate abort } S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \rangle}$$

Weak Immediate Abort

$$\frac{[\![\sigma]\!]_{\mathcal{E}} = \text{true} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, t \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak immediate abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{[\![\sigma]\!]_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak immediate abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{[\![\sigma]\!]_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak immediate abort } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \left[\begin{array}{l} \text{weak immediate abort } S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \rangle}$$

Strong Delayed Suspend

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \text{suspend } S \text{ when}(\sigma) \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \text{suspend } S \text{ when}(\sigma) \rangle \rightarrow_{\mathbb{Q}} \left\langle \begin{array}{l} \text{immediate suspend } S' \\ \text{when}(\sigma) \end{array} \right\rangle, \mathcal{D}, \text{false} \rangle}$$

Strong Immediate Suspend

$$\begin{array}{c}
 \frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true}}{\langle \mathcal{E}, \left[\begin{array}{l} \text{immediate suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \left[\begin{array}{l} \text{immediate suspend } S \\ \text{when}(\sigma) \end{array} \right], \{\}, \text{false} \rangle} \\
 \\
 \frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{immediate suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle} \\
 \\
 \frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{immediate suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \left[\begin{array}{l} \text{immediate suspend } S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \rangle}
 \end{array}$$

Weak Delayed Suspend

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{\langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \left[\begin{array}{l} \text{weak immediate suspend } S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \rangle}$$

Weak Immediate Suspend

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, t \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak immediate suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \left\langle \left[\begin{array}{l} \text{weak immediate suspend } S \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \right\rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{true} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak immediate suspend } S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \mathcal{D}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \text{ and } \langle \mathcal{E}, S \rangle \rightarrow_{\mathbb{Q}} \langle S', \mathcal{D}, \text{false} \rangle}{\langle \mathcal{E}, \left[\begin{array}{l} \text{weak immediate suspend} \\ S \\ \text{when}(\sigma) \end{array} \right] \rangle \rightarrow_{\mathbb{Q}} \left\langle \left[\begin{array}{l} \text{weak immediate suspend} \\ S' \\ \text{when}(\sigma) \end{array} \right], \mathcal{D}, \text{false} \right\rangle}$$

Computing the Reaction

- using the SOS transition rules, we can compute the statement that has to be executed in the next macro step
- this is the update of the internal control flow state
- it remains to determine the outputs for given inputs and a given statement
- to this end, we make use of SOS reaction rules
- these rules work with an incomplete environment $\mathcal{E}$
- this environment $\mathcal{E}$ is then completed step by step
- to this end, we estimate the sets of assignments that must/can be executed

Incomplete Environments

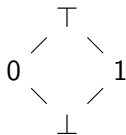
- to compute the reactions,
we start with an incomplete environment $\mathcal{E}_0$
- $\mathcal{E}_0$ has known values for all inputs
- but has no values for the outputs
- to this end, we make use of the value $\perp$ that indicates that a value is not yet known
- initially, we thus have $\mathcal{E}_0(x) = \perp$ for all outputs
- we also introduce $\top$ meaning that the value cannot be computed due to a runtime error

Information Ordering

- on $\mathcal{D} \cup \{\perp, \top\}$, we introduce a partial order relation $\preceq$ as follows

$$x \preceq y :\Leftrightarrow (x = \perp) \vee (x = y) \vee (y = \top)$$

- $x \preceq y$ means that y contains more information than x
- $\preceq$ is not a total order (e.g. neither $0 \preceq 1$ nor $1 \preceq 0$ holds)
- $\preceq$ is a lattice, since for all elements x, y , we have $\sup(\{x, y\})$ and $\inf(\{x, y\})$
- e.g. for booleans, we have



sup()	$\perp$	0	1	$\top$
$\perp$	$\perp$	0	1	$\top$
0	0	0	$\top$	$\top$
1	1	$\top$	1	$\top$
$\top$	$\top$	$\top$	$\top$	$\top$

inf()	$\perp$	0	1	$\top$
$\perp$	$\perp$	$\perp$	$\perp$	$\perp$
0	$\perp$	0	$\perp$	0
1	$\perp$	$\perp$	1	1
$\top$	$\perp$	0	1	$\top$

Four-Valued Logic

- we extend all operations on $\mathcal{D} \cup \{\perp, \top\}$
- typically $\perp \circledast x = \perp$
- however, sometimes $\perp \circledast x \neq \perp$, since the result is already determined by one of the arguments, e.g.:

$\wedge$	$\perp$	0	1	$\top$
$\perp$	$\perp$	0	$\perp$	$\top$
0	0	0	0	$\top$
1	$\perp$	0	1	$\top$
$\top$	$\top$	$\top$	$\top$	$\top$

$\vee$	$\perp$	0	1	$\top$
$\perp$	$\perp$	$\perp$	1	$\top$
0	$\perp$	0	1	$\top$
1	1	1	1	$\top$
$\top$	$\top$	$\top$	$\top$	$\top$

x	$\neg x$
$\perp$	$\perp$
0	1
1	0
$\top$	$\top$

- Why are these four-valued extensions of the boolean operations chosen?

Four-Valued Logic

- Are the definitions of the four-valued functions unique?
- we have to maintain the values for boolean operands, and we have to enforce monotonicity:

$$x_1 \preceq x_2 \wedge y_1 \preceq y_2 \rightarrow x_1 \circledast y_1 \preceq x_2 \circledast y_2$$

- to allow progress of information, we also want $\perp \wedge 0 = 0 \wedge \perp = 0$ and analogously $\perp \vee 1 = 1 \vee \perp = 1$
- based on these requirements, still many definitions are possible (see next slide)

$\Rightarrow$ the greatest ones are chosen

- further enforcing commutativity for conjunction and disjunction reduces this to nine definitions (greatest ones are retained)

Four-Valued Logic (81 Solutions for Conjunction)

[1](#)Figures/LatticeConjunctionFull-eps-converted-to.pdf

Four-Valued Logic (81 Solutions for Disjunction and Conjunction)

[1](#)Figures/LatticeDisjunctionFull-eps-converted-to.pdf

Four-Valued Logic (All Solutions for Commutative Con-/Disjunction)

- there are nine solutions
- these form a lattice
- hence, there is a greatest and a least four-valued extension
- one could prefer the one that contains most disjunctive values (as with `if-converted-to.p`)
- or the greatest solution which is strict in the sense that one operand's failed evaluation is not overwritten by the other operand
- in what follows, the particular choice does not matter

1-Figures/Lattice

1-Figures/Lattice

1-Figures/Lattice

1-Figures/Lattice

Lattice of Incomplete Environments

- for a given program P , we consider the set $\mathfrak{E}_P$ of all incomplete environments $\mathcal{E}$ mapping the variables of P to values according to their types including $\perp$ and $\top$
- we also introduce a partial order relation on environments as follows

$$\mathcal{E}_1 \sqsubseteq \mathcal{E}_2 :\Leftrightarrow \forall x \in \mathcal{V}. \mathcal{E}_1(x) \preceq \mathcal{E}_2(x)$$

- $\mathcal{E}_1 \sqsubseteq \mathcal{E}_2$ means that $\mathcal{E}_2$ contains more information than $\mathcal{E}_1$
- $(\mathfrak{E}_P, \sqsubseteq)$ is a lattice:
 - $\mathcal{E}_{\text{sup}}(x) := \sup(\{\mathcal{E}_1(x), \mathcal{E}_2(x)\})$
 - $\mathcal{E}_{\text{inf}}(x) := \inf(\{\mathcal{E}_1(x), \mathcal{E}_2(x)\})$

Evaluation of Expressions using incomplete Environments

- given an incomplete environment $\mathcal{E}$, and a program expression σ , we define its evaluation $\llbracket \sigma \rrbracket_{\mathcal{E}}$ as expected, i.e.,
 - for variables x , we define $\llbracket x \rrbracket_{\mathcal{E}} := \mathcal{E}(x)$
 - for operators $\otimes$, we define $\llbracket \sigma_1 \otimes \sigma_2 \rrbracket_{\mathcal{E}} := \llbracket \sigma_1 \rrbracket_{\mathcal{E}} \otimes \llbracket \sigma_2 \rrbracket_{\mathcal{E}}$
- to this end, we make use of the function tables
- and can thus sometimes evaluate expressions where one argument is $\perp$ to values different to $\perp$
- this way, we can evaluate expressions to known values even though the environment is incomplete
- this can be used to obtain a progress in information

Current Reactions as Fixpoints

- for a program P , we will define a function f_P that maps an environment $\mathcal{E} \in \mathfrak{E}_P$ to another environment $f_P(\mathcal{E}) \in \mathfrak{E}_P$
- f_P will be continuous w.r.t. $\sqsubseteq$,
and thus, we can compute its least fixpoint
- the current reaction $\mathcal{E}$ of P is the least fixpoint of f_P
- P is causally correct iff all variables have known values in $\mathcal{E}$
- the definition of f_P is however not that easy
- we first have to consider SOS reaction rules and will then define f_P

SOS Reaction Rules

SOS Reaction Rule

$$\langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle$$

- $\mathcal{E}$: environment of the current macro step
- S : statement to be executed
- $\mathcal{D}_{\text{must}}$: immediate actions that must be executed
- $\mathcal{D}_{\text{can}}$: immediate actions that can be executed
- t_{must} : holds iff S must be instantaneously executed
- t_{can} : holds iff S can be instantaneously executed
- note $\mathcal{D}_{\text{must}} \subseteq \mathcal{D}_{\text{can}}$ and $t_{\text{must}} \rightarrow t_{\text{can}}$

SOS Reaction Rules

- SOS reaction rules are recursively defined for the statements
- analogously to the SOS transition rules, we have assumptions and conclusions
- we now consider the SOS reaction rules for all core statements
- and then continue with the definition of the current reaction as a least fixpoint
- t_{must} and t_{can} encode instantaneous execution as follows:

t_{must}	t_{can}	<i>instant</i>
false	false	false
false	true	$\perp$
true	false	--
true	true	true

Atomic Statements

$$\langle \mathcal{E}, \text{nothing} \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \{\}, \text{true}, \text{true} \rangle$$

$$\langle \mathcal{E}, \ell : \text{pause} \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \{\}, \text{false}, \text{false} \rangle$$

$$\langle \mathcal{E}, x = \tau \rangle \mapsto_{\mathbb{Q}} \langle \{(x = \tau)\}, \{(x = \tau)\}, \text{true}, \text{true} \rangle$$

$$\langle \mathcal{E}, \text{next}(x) = \tau \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \{\}, \text{true}, \text{true} \rangle$$

Conditional

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \quad \langle \mathcal{E}, S_1 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, t_{\text{must}}^1, t_{\text{can}}^1 \rangle}{\langle \mathcal{E}, \text{if}(\sigma) \ S_1 \ \text{else} \ S_2 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, t_{\text{must}}^1, t_{\text{can}}^1 \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \quad \langle \mathcal{E}, S_2 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}{\langle \mathcal{E}, \text{if}(\sigma) \ S_1 \ \text{else} \ S_2 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \perp \quad \langle \mathcal{E}, S_1 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, t_{\text{must}}^1, t_{\text{can}}^1 \rangle \quad \langle \mathcal{E}, S_2 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}{\langle \mathcal{E}, \text{if}(\sigma) \ S_1 \ \text{else} \ S_2 \rangle \rightsquigarrow_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1 \cap \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^1 \cup \mathcal{D}_{\text{can}}^2, t_{\text{must}}^1 \wedge t_{\text{must}}^2, t_{\text{can}}^1 \vee t_{\text{can}}^2 \rangle}$$

Sequence

$$\frac{\langle \mathcal{E}, S_1 \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, \text{false}, \text{false} \rangle}{\langle \mathcal{E}, \{S_1; S_2\} \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, \text{false}, \text{false} \rangle}$$

$$\frac{\langle \mathcal{E}, S_1 \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, \text{false}, \text{true} \rangle \quad \langle \mathcal{E}, S_2 \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}{\langle \mathcal{E}, \{S_1; S_2\} \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1 \cup \mathcal{D}_{\text{can}}^2, \text{false}, t_{\text{can}}^2 \rangle}$$

$$\frac{\langle \mathcal{E}, S_1 \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, \text{true}, \text{true} \rangle \quad \langle \mathcal{E}, S_2 \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}{\langle \mathcal{E}, \{S_1; S_2\} \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1 \cup \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^1 \cup \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}$$

Parallel Statement

$$\frac{\langle \mathcal{E}, S_1 \rangle \mathrel{\mathcal{Q}}_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1, \mathcal{D}_{\text{can}}^1, t_{\text{must}}^1, t_{\text{can}}^1 \rangle \quad \langle \mathcal{E}, S_2 \rangle \mathrel{\mathcal{Q}}_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^2, t_{\text{must}}^2, t_{\text{can}}^2 \rangle}{\langle \mathcal{E}, \{S_1 \parallel S_2\} \rangle \mathrel{\mathcal{Q}}_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}^1 \cup \mathcal{D}_{\text{must}}^2, \mathcal{D}_{\text{can}}^1 \cup \mathcal{D}_{\text{can}}^2, t_{\text{must}}^1 \wedge t_{\text{must}}^2, t_{\text{can}}^1 \wedge t_{\text{can}}^2 \rangle}$$

Loops

$$\frac{\langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{do } S \text{ while}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false}}{\langle \mathcal{E}, \text{while}(\sigma) \ S \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \{\}, \text{true}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{while}(\sigma) \ S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \perp \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{while}(\sigma) \ S \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \mathcal{D}_{\text{can}}, \text{false}, \text{true} \rangle}$$

Abort (1/2)

$$\frac{\langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, [\text{weak}] \text{ abort } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, [\text{weak}] \text{ immediate abort } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{weak immediate abort } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, \text{true}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true}}{\langle \mathcal{E}, \text{immediate abort } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \{\}, \text{true}, \text{true} \rangle}$$

Abort (2/2)

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \perp \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{weak immediate abort } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, \text{true} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \perp \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{immediate abort } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \mathcal{D}_{\text{can}}, t_{\text{must}}, \text{true} \rangle}$$

Suspend (1/2)

$$\frac{\langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, [\text{weak}] \text{ suspend } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{false} \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, [\text{weak}] \text{ immediate suspend } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true} \quad \langle \mathcal{E}, S \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{weak immediate suspend } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, \text{false}, \text{false} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \text{true}}{\langle \mathcal{E}, \text{immediate suspend } S \text{ when}(\sigma) \rangle \mapsto_{\mathbb{Q}} \langle \{\}, \{\}, \text{false}, \text{false} \rangle}$$

Suspend (2/2)

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \perp \quad \langle \mathcal{E}, S \rangle \mathrel{\mathcal{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{immediate suspend } S \text{ when}(\sigma) \rangle \mathrel{\mathcal{Q}} \langle \{\}, \mathcal{D}_{\text{can}}, \text{false}, t_{\text{can}} \rangle}$$

$$\frac{\llbracket \sigma \rrbracket_{\mathcal{E}} = \perp \quad \langle \mathcal{E}, S \rangle \mathrel{\mathcal{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, t_{\text{must}}, t_{\text{can}} \rangle}{\langle \mathcal{E}, \text{weak immediate suspend } S \text{ when}(\sigma) \rangle \mathrel{\mathcal{Q}} \langle \mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, \text{false}, t_{\text{can}} \rangle}$$

Current Reactions as Fixpoints

- recall that we wanted to define a function f_P for a program P that maps environments to environments so that $\mathcal{E} \sqsubseteq f_P(\mathcal{E})$ holds
- to this end, we will make use of the SOS reaction rules
- we assume that the SOS reaction rules are implemented in a function `ReactSOS` that computes for inputs $(\mathcal{E}, S)$ the pair $(\mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}})$ (we do not need $t_{\text{must}}, t_{\text{can}}$)
- by $\mathcal{D}_{\text{must}}$ and $\mathcal{D}_{\text{can}}$, we then determine a new environment $\mathcal{E}'$

Completing Environments – Function f_P

- by $\mathcal{D}_{\text{must}}$ and $\mathcal{D}_{\text{can}}$, we then determine a new environment $\mathcal{E}'$ as follows:
- define $\mathcal{D}_{\text{must}}^x$ as the assignments $x=\tau$ of $\mathcal{D}_{\text{must}}$ writing to x
 - let $\mathcal{D}_{\text{must}}^x = \{x=\tau_1, \dots, x=\tau_n\} \neq \{\}$
 - update $\mathcal{E}$ such that $\mathcal{E}(x) := \sup \{\llbracket x \rrbracket_{\mathcal{E}}, \llbracket \tau_1 \rrbracket_{\mathcal{E}}, \dots, \llbracket \tau_n \rrbracket_{\mathcal{E}}\}$
 - note that writing $\perp$ and a known value v yields $\mathcal{E}(x) = v$
 - note that writing value $v_1 \neq v_2$ with $v_i \neq \perp$ yields $\mathcal{E}(x) = \top$
- define $\mathcal{D}_{\text{can}}^x$ as the assignments $x=\tau$ of $\mathcal{D}_{\text{can}}$ writing to x
 - assume $\mathcal{D}_{\text{can}}^x = \{\} \implies$ no immediate assignment can write to x
 - if $\llbracket x \rrbracket_{\mathcal{E}} \neq \perp$, x already has a value due to delayed assignments of the previous step
 - thus, we apply the reaction to absence if $\mathcal{D}_{\text{can}}^x = \{\} \wedge \llbracket x \rrbracket_{\mathcal{E}} = \perp$:
 - if x is an event variable, set $\mathcal{E}(x)$ to its default value
 - if x is a memorized variable, set $\mathcal{E}(x)$ to the previous value (or the default value when the initial reaction is computed)

Current Reaction as Least Fixpoint

- the updates described on the previous slide define a function `UpdateEnv` (which is the previously mentioned function f_P)
- recall our partial orders
 - $x \preceq y :\Leftrightarrow (x = \perp) \vee (x = y) \vee (y = \top)$
 - $\mathcal{E}_1 \sqsubseteq \mathcal{E}_2 :\Leftrightarrow \forall x \in \mathcal{V}. \mathcal{E}_1(x) \preceq \mathcal{E}_2(x)$
- it is easily seen that `UpdateEnv` is monotonous
- since P has only finitely many variables, we have a finite lattice
- and therefore `UpdateEnv` is continuous, and thus, we can compute its least fixpoint by the Tarski-Knaster iteration
- **current reaction is the least fixpoint of `UpdateEnv`**

Compute Current Reaction

```
function ComputeReaction( $\mathcal{E}, S, \mathcal{E}_{\text{pre}}$ )  
  do  
     $\mathcal{E}_{\text{old}} := \mathcal{E};$   
     $(\mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}) := \text{ReactSOS}(\mathcal{E}, S);$   
     $\mathcal{E} := \text{UpdateEnv}(\mathcal{D}_{\text{must}}, \mathcal{D}_{\text{can}}, \mathcal{E}_{\text{old}}, \mathcal{E}_{\text{pre}})$   
  while  $\mathcal{E}_{\text{old}} \neq \mathcal{E}$   
  return  $\mathcal{E}$ 
```

- starting with an incomplete environment $\mathcal{E}$ and the previous environment $\mathcal{E}_{\text{pre}}$, we compute $\mathcal{D}_{\text{must}}$ and $\mathcal{D}_{\text{can}}$ by the SOS reaction rules
- then, we update the environment $\mathcal{E}$ by $\mathcal{D}_{\text{must}}$ and $\mathcal{D}_{\text{can}}$
- and repeat this until a fixpoint is obtained

Quartz Interpreter

```

function InterpretQuartz( $S$ )
   $\mathcal{E}_{\text{pre}} := \mathcal{E}_{\text{def}}$ ; // default values for all variables
   $\mathcal{D}_{\text{del}} := \{\}$ ; // no delayed actions at the beginning
  do
     $\mathcal{E}_{\text{in}} := \text{ReadInputs}()$ ;
     $\mathcal{E}_{\text{init}} := \text{UpdateDelayedActs}(\mathcal{D}_{\text{del}}, \mathcal{E}_{\text{pre}})$ ;
     $\mathcal{E} := \text{ComputeReaction}(\mathcal{E}_{\text{init}}, S, \mathcal{E}_{\text{pre}})$ ;
    if  $\exists x \in \mathcal{V}. \mathcal{E}(x) \in \{\perp, \top\}$  then fail;
     $(S', \mathcal{D}, t) := \text{TransSOS}(S, \mathcal{E})$ ;
     $\mathcal{D}_{\text{del}} := \text{delayed actions of } \mathcal{D}$ ;
     $S := S'$ ;
     $\mathcal{E}_{\text{pre}} := \mathcal{E}$ ;
  while( $\neg t$ );
  
```

Constructive Programs

- the interpreter on the previous slide computes the reactions and control flow updates for any constructive program
 - after reading new inputs, we have $\mathcal{E}_{\text{in}}$
 - we then evaluate the delayed assignments $\mathcal{D}_{\text{del}}$ obtained by SOS transition rules of the previous reaction in the previous environment $\mathcal{E}_{\text{pre}}$ and update $\mathcal{E}_{\text{in}}$ thereby to $\mathcal{E}_{\text{init}}$
 - using $\mathcal{E}_{\text{init}}$, we compute $\mathcal{E}$ by ComputeReaction (i.e. SOS reaction rules)
 - using $\mathcal{E}$, we compute $(S', \mathcal{D}, t)$ by the SOS transition rules
 - we repeat this until the program terminates
- programs where the least fixpoint contains either $\perp$ or $\top$ are not causally correct
- therefore the interpreter fails in these cases

Relationship between 2-Value and 4-Valued Solutions

- consider any function $f(x_0, \dots, x_{n-1}) = (\varphi_0, \dots, \varphi_{n-1})$ with propositional logic formulas φ on the variables $x_0, \dots, x_{n-1}$
- let $\check{x} = (\check{x}_0, \dots, \check{x}_{n-1})$ be the least fixpoint, i.e., $\check{x} = f(\check{x})$
- Theorem: If $\check{x}_i \in \mathbb{B}$, then we have for all $y \in \mathbb{B}^n$ with $y = f(y)$ that $y_i = \check{x}_i$. The converse is however false.
- proof:
 - consider any boolean solution $y \in \mathbb{B}^n$ with $y = f(y)$
 - $\check{x} \preceq y$ since y is a fixpoint and $\check{x}$ is the least fixpoint
 - hence, $\check{x}_i \preceq y_i$ holds for $i = 0, \dots, n-1$
 - if both $\check{x}_i \in \mathbb{B}$ and $y_i \in \mathbb{B}$ holds, this implies $\check{x}_i = y_i$
- Corollary: If $\check{x}$ is the least fixpoint of f , and $\check{x} \in \mathbb{B}^n$ holds, then this is the unique boolean solution of $x = f(x)$.

Relationship between 2-Value and 4-Valued Solutions

- we have proved that boolean values in the least fixpoint enforce that value also in all boolean solutions
- the converse does not hold: there are functions f where all solutions of $x = f(x)$ share the same boolean value x_i , but still the least fixpoint is not that value
- consider the example (left hand side) and its boolean solutions (right hand side)

$$\left\{ \begin{array}{l} x_0 = \neg x_0 \wedge \neg x_1 \\ x_1 = x_2 \vee x_3 \\ x_2 = x_2 \\ x_3 = x_3 \end{array} \right.$$

x_0	x_1	x_2	x_3
0	1	0	1
0	1	1	0
0	1	1	1

- the least fixpoint is $\check{x} = (\perp, \perp, \perp, \perp)$
- but all boolean solutions (as shown above) agree on $x_0 = 0$

Relationship between 2-Value and 4-Valued Solutions

- so, we know that boolean values in $\check{x}$ are unique for all boolean solutions
- Can we say anything about boolean solutions in case $\check{x}_i = \perp$?
- the following examples show that we have no further information, in particular:
 - there may be no boolean solution
 - there may be a unique boolean solution
 - there may be many boolean solutions

Module P01

Example

```
module P01(event ?i , o1 , o2 , o3) {
  if(i)    emit(o1);
  if(!o1)  emit(o2);
  if(o2)   emit(o3);
}
```

if i is 0, then

	i	o1	o2	o3	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	0	$\perp$	$\perp$	$\perp$	$\{\}$	$\{\text{emit}(o2), \text{emit}(o3)\}$
$\mathcal{E}_1$	0	0	$\perp$	$\perp$	$\{\text{emit}(o2)\}$	$\{\text{emit}(o2), \text{emit}(o3)\}$
$\mathcal{E}_2$	0	0	1	$\perp$	$\{\text{emit}(o2), \text{emit}(o3)\}$	$\{\text{emit}(o2), \text{emit}(o3)\}$
$\mathcal{E}_3$	0	0	1	1	$\{\text{emit}(o2), \text{emit}(o3)\}$	$\{\text{emit}(o2), \text{emit}(o3)\}$

with transition $\langle \mathcal{E}_3, S \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\text{emit}(o2), \text{emit}(o3)\}, \text{true} \rangle$

Module P01

Example

```
module P01(event ?i , o1 , o2 , o3) {
  if(i)    emit(o1);
  if(!o1)  emit(o2);
  if(o2)   emit(o3);
}
```

if i is 1, then

	i	$o1$	$o2$	$o3$	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	1	$\perp$	$\perp$	$\perp$	$\{\text{emit}(o1)\}$	$\{\text{emit}(o1), \text{emit}(o2), \text{emit}(o3)\}$
$\mathcal{E}_1$	1	1	$\perp$	$\perp$	$\{\text{emit}(o1)\}$	$\{\text{emit}(o1), \text{emit}(o3)\}$
$\mathcal{E}_2$	1	1	0	$\perp$	$\{\text{emit}(o1)\}$	$\{\text{emit}(o1)\}$
$\mathcal{E}_3$	1	1	0	0	$\{\text{emit}(o1)\}$	$\{\text{emit}(o1)\}$

with transition $\langle \mathcal{E}_3, S \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\text{emit}(o1)\}, \text{true} \rangle$

Module P02

Example

```
module P02(event o1, o2) {
  emit(o2);
  if(!o1) {
    if(o2) w: pause;
    emit(o1);
  }
}
```

initial reaction:

	o1	o2	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	$\perp$	$\perp$	$\{\text{emit}(o2)\}$	$\{\text{emit}(o1), \text{emit}(o2)\}$
$\mathcal{E}_1$	$\perp$	1	$\{\text{emit}(o2)\}$	$\{\text{emit}(o2)\}$
$\mathcal{E}_2$	0	1	$\{\text{emit}(o2)\}$	$\{\text{emit}(o2)\}$

with transition $\langle \mathcal{E}_2, S \rangle \rightarrow_{\mathbb{Q}} \langle \text{emit}(o1), \{\text{emit}(o2)\}, \text{false} \rangle$

Module P02

Example

```

module P02(event o1,o2) {
    emit(o2);
    if(!o1) {
        if(o2) w: pause;
        emit(o1);
    }
}

```

second reaction (executing $S' = \text{emit}(o1)$):

	o1	o2	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	$\perp$	$\perp$	$\{\text{emit}(o1)\}$	$\{\text{emit}(o1)\}$
$\mathcal{E}_1$	1	0	$\{\text{emit}(o1)\}$	$\{\text{emit}(o1)\}$

with transition $\langle \mathcal{E}_1, S' \rangle \rightarrow_{\mathbb{Q}} \langle \text{nothing}, \{\text{emit}(o1)\}, \text{true} \rangle$

Modules P03 and P04

Example

```
module P03(event o) {
  if(!o) emit(o);
}

module P04(event o) {
  if(o) emit(o);
}
```

- there is no way to run these programs!
- P03 does not even have satisfying behaviors
- P04 could be satisfied by both $o=\text{true}$ and $o=\text{false}$
- none of the programs is causally correct

	o	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	$\perp$	$\{\}$	$\{\text{emit}(o)\}$
$\mathcal{E}_1$	$\perp$	$\{\}$	$\{\text{emit}(o)\}$

Modules P05 and P06

Example

```

module P05(event o1,o2) {
    if(o1)   emit(o1);
    if(!o2)  emit(o2);
}

module P06(event o1,o2) {
    if(o1)   emit(o2);
    if(o2)   emit(o1);
}

```

- there is no way to run these programs!
- P05 does not even have satisfying behaviors
- P06 could be satisfied by both $o1=o2=\text{true}$ and $o1=o2=\text{false}$
- none of the programs is causally correct

	o1	o2	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	$\perp$	$\perp$	$\{\}$	$\{\text{emit}(o1), \text{emit}(o2)\}$
$\mathcal{E}_1$	$\perp$	$\perp$	$\{\}$	$\{\text{emit}(o1), \text{emit}(o2)\}$

Modules P07a and P07b

Example

```

module P07a(event o) {
  if(o) w: pause;
  emit(o);
}

module P07b(event o) {
  if(!o) w: pause;
  emit(o);
}

```

- P07a has no behavior
 - assume o would be false, then
 - the control would not stop at label w
 - and thus would execute emit(o)
 - **but then, o would be true**
 - assume o would be true, then
 - the control would stop at label w
 - and thus would not execute emit(o)
 - **but then, o would be false**
- ⇒ there is no behavior (same with P07b)

	o	$\mathcal{D}_{\text{must}}$	$\mathcal{D}_{\text{can}}$
$\mathcal{E}_0$	$\perp$	$\{\}$	$\{\text{emit}(o)\}$
$\mathcal{E}_1$	$\perp$	$\{\}$	$\{\text{emit}(o)\}$

Module P08

Example

```
module P08(event ?i,o1,o2) {  
  weak immediate abort {  
    {  
      if(!i) w: pause;  
      emit(o1);  
    }  
    ||  
    if(o1) emit(o2);  
  } when(o2);  
  emit(o1);  
}
```

- the program is not constructive for $i == \text{false}$ (two behaviors possible)

- assume i would be false, then
 - the control would stop at label w
 - and thus would not execute the first `emit(o1)`
 - at this stage, **we have to speculate about the value of $o1$**
 - assume $o1$ would be false, then
 - we would not execute `emit(o2)`
 - no abortion takes place, and the control will remain at label w
 - assume $o1$ would be true, then
 - we would execute `emit(o2)`
 - the abortion takes place, and the control will leave label w
 - thus, the second `emit(o1)` is executed and justifies our

Module P08

Example

```
module P08(event ?i,o1,o2) {  
  weak immediate abort {  
    {  
      if(!i) w: pause;  
      emit(o1);  
    }  
    ||  
    if(o1) emit(o2);  
  } when(o2);  
  emit(o1);  
}
```

- assume i would be true, then
 - the control would not stop at label w and instead executes $\text{emit}(o1)$
 - thus, also $\text{emit}(o2)$ is executed
 - the weak abortion takes place, but has no effect
 - we then execute the second $\text{emit}(o1)$
 - thus, $o1=o2=\text{true}$ and nothing is left for further macro steps

Module P09

Example

```
module P09(event o1,o2) {  
    if(o1) emit(o1);  
    ||  
    if(o1)  
        if(!o2) emit(o2);  
}
```

- P09 is not constructive
- note that `if(!o2) emit(o2);` is a contradiction
- thus, `o1=true` would lead to a contradiction
- thus logically only `o1=o2=false` makes sense
- however, this is not constructively found

Module P10

Example

```
module P10(event o) {  
  if(o) nothing;  
  emit(o);  
}
```

- in Quartz, P10 is constructive, while in Esterel, it is not
-

Module P11

Example

```
module P11(event o1,o2) {  
  if(o1) {  
    emit(o2);  
    if(o2) w: pause;  
    emit(o1);  
  }  
}
```

- use the simulator!

Module P12

Example

```
module P12(event o) {  
  if(o) emit(o);  
  else emit(o);  
}
```

- use the simulator!

Module P13

Example

```
module P13(event ?i,o1,o2) {  
  if(i) {  
    if(o1) emit(o2);  
  } else {  
    if(o2) emit(o1);  
  }  
}
```

- use the simulator!

Module P14

Example

```
module P14(event o1,o2) {  
  if(o1) emit(o2);  
  w: pause;  
  if(!o2) emit(o1);  
}
```

- use the simulator!

Module P15

Example

```
module P15(event o1,o2) {  
    emit(o2);  
    if(o1)  
        if(!o2) emit(o1);  
}
```

- use the simulator!

Module P16

Example

```
module P16(event o) {  
  if(o)  
    if(!o) emit(o);  
}
```

- use the simulator!

Module P17

Example

```
module P17(event o1,o2) {  
  if(o1) {  
    emit(o2);  
    if(!o2) emit(o1);  
  }  
}
```

- use the simulator!

Module P18

Example

```
module P18(event o1,o2) {  
  if(o1) {  
    emit(o2);  
    ||  
    if(!o2) emit(o1);  
  }  
}
```

- use the simulator!

Module P19

Example

```
module P19(event o1,o2) {  
  if(o1) {  
    emit(o2);  
    ||  
    if(o2) emit(o1);  
  }  
}
```

- use the simulator!

Module P20

Example

```
module P20(event o1,o2,o3,o4) {  
    if(o2) emit(o1);  
    ||  
    if(o1 & o3) emit(o2);  
    ||  
    if(!o1 & o4) emit(o2);  
    ||  
    emit(o3);  
    ||  
    emit(o4);  
}
```

- use the simulator!

Module P21

Example

```
module P21(event ?i1 ,? i2 ,o1 ,o2) {  
  {  
    if(o1 & i1) emit(o2);  
    ||  
    if(o2 & i2) emit(o1);  
  }  
}
```

- use the simulator!

References and Further Reading I